#### DESKTOP PLATFORM



# The world's first Arbitrary Waveform Transceiver

Introducing Tabor's all new Proteus series, the world's first Arbitrary Waveform Transceiver. In a compact desktop platform, the system integrates the ability to transmit, receive and perform digital signal processing all in a single instrument. The small footprint system, that can generate up to 12 channels in a single box, offers industry leading performance, various configuration options, an innovative task oriented programming, and user programmable FPGA. So whether it is for aerospace and defense, telecommunications, automotive, medical or high-end physics applications Proteus opens the door to a world of infinite possibilities.

### Leading Features:



Dual, four, eight or twelve channel 1.25GS/s & 2.5 GS/s 16 bit, or dual, four or six channel 9GS/s 16 bit, AWG & AWT configurations



Integrated NCO for digital up-converting to microwave frequencies

Real time data streaming directly to the FPGA for continuous and infinite waveform generation



9GHz Bandwidth, 2.7GS/s 12 bit digitizer option for feedback control system and conditional waveform generation

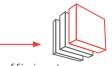
Innovative task oriented sequence programming for maximum flexibility to generate any imaginable scenario

Up to 16GS/s waveform memory with the ability to simultaneously generate and download waveforms.



Excellent phase noise and spurious performance

User customizable FPGA for application specific solutions



Space efficient desktop platform, with USB 3.0, 10G Ethernet and thunderbolt high speed interfaces.

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PROTEUS Infinite possibilities

### **Compact and space efficient**

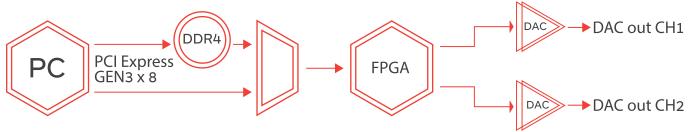
The desktop version of the Proteus series offers up to 12 channels in a 4U, half 19" dedicated chassis. The compact form size and small footprint saves valuable bench space. So for synchronized, phase coherent, multi-channel applications such as quantum physics and radar applications the Proteus arbitrary waveform transceiver is an ideal, space efficient and cost effective solution.

## Extra-fast communication interface

Spending more time setting up your generated scenario than actually running it? The Proteus desktop platform offers the fastest standardized communication interfaces commonly available in PCs today. These include USB3.0 and 10GE interfaces as well as, a thunderbolt 3 interface which enables up to 40Gb/s of data transfer speed. These enable the user to easily connect to the Proteus arbitrary waveform transceiver and still offer some of the fastest waveform download available on the market today, saving you one of your most valuable resources, time.

### Feedback control system

Many of today's applications, require conditional waveform generation depending on input signals from the environment. The Proteus arbitrary waveform transceiver flawlessly integrates both DAC and ADC in one system, controlled by a single FPGA for optimal synchronization and minimum latency. This high speed control system provides a feedback loop for fast decision making on the fly with minimum latency.



### Generate any imaginable scenario

The new series offers an innovative task oriented sequence programming where user can change the full instrument set up at every line of the task table. In addition, not only can users of the Proteus series instruments generate and download waveforms simultaneously, they can stream data directly to the FPGA without the need to use the built in memory. This enables generating random, unique and infinitely long scenarios directly from the controlling PC at DAC speeds of up to 6GS/s. So no matter whether your scenario is extremely complex, infinite or even dynamic you can generate it with the Proteus series model.



GENERAL CHARACTERISTICS	P9082/4/6D	P2582/4/8/12D	P1282/4/8/12D		
MAX. SAMPLE RATE	9GS/s	2.5GS/s	1.25GS/s		
RESOLUTION		16-bit <sup>(1)</sup>			
ENOB AT MAX. FREQUENCY		TBD			
NUMBER OF CHANNELS	8/6/24	8/8/16/24	4/4/8/12		
BANDWIDTH	9GHz <sup>(2)</sup> 4.5GHz <sup>(3)</sup>	5GHz <sup>(2)</sup> 2.5GHz <sup>(3)</sup>	2.5GHz <sup>(2)</sup> 1.25GHz <sup>(3)</sup>		
MEMORY	Up to 16GS	Up to 8GS	Up to 4GS		
INTERFACE	USB 3.0, 10GE, Thunderbolt 3				
LATENCY / SYSTEM DELAY	200ns				
FINE DELAY	-5ns to 5ns				
DELAY RESOLUTION	5ps resolution				
COARSE DELAY	0 to wavelength in 1 sample point resolution				
INITIAL SKEW BETWEEN CHANNELS	Ops				

(1) Depending on sampling mode (2) Direct output option (3) DC output option

ARBITRARY / TASK TABLE	P9082/4/6D	P2582/4/8/12D	P1282/4/8/12D
MINIMUM SEGMENT LENGTH NORMAL FAST SEGMENT	2048 points1024 points128 points64 points		
WAVEFORM GRANULARITY STANDARD OPTIONAL	64 points 32 points	32 points 16 points	32 points 16 points
SEGMENTS	2^15		
SEGMENT LOOPS	2^20		
SEQUENCES	2^15		
SEQUENCE TABLE ENTRIES	2^15		
SEQUENCE LOOPS	2^20		
ADVANCED SEQUENCES TABLE ENTRIES	1024		

SIGNAL PURITY	DC OUTPUT	DIRECT OUTPUT		
HARMONIC DISTORTION				
fout = 100 MHz	<-75 dBc (typ)	<-80 dBc (typ)		
fout = 10 MHz - 500 MHz, DC to 2 GHz	<-70 dBc (typ)	<-75 dBc (typ)		
fout = 10 MHz 3 GHz, DC to 4.5 GHz	<-65 dBc (typ)	<-70 dBc (typ)		
fout = 10 MHz 7 GHz, 5 to 10 GHz		TBD		
SFDR				
fout = 10 MHz1 GHz DC to 1 GHz	-85 dBc (typ)	<-85 dBc (typ)		
fout = 1 GHz3 GHz , DC to 3 GHz	-75 dBc (typ	<-75 dBc (typ)		
fout = 3 GHz4.5 GHz , DC to 4.5 GHz	-65 dBc (typ)	<-65 dBc (typ)		
fout = 3 GHz4.5 GHz , DC to 4.5 GHz	100us Full bandwidth	-<70 dBc (typ)		
fout = 7 GHz, 6 to 8 GHz (2nd Nyquist)	<6us Narrow bandwidth (<10% BW)	-<70 dBc (typ)		
PHASE NOISE (@10kHz offset)				
fout = 187.5MHz	-130 dE	-130 dBc/Hz		
fout = 375MHz	-125 dBc/Hz			
fout = 750MHz	-120 dBc/Hz			
fout = 2GHz – 5GHz	-110 dBc/Hz			
fout = 5GHz - 7GHz	-105 dBc/Hz			







DC OUTPUT		DIRECT OUTPUT (OPTIONAL)	
OUTPUT TYPE	Single-ended or differentia DC-coupled	OUTPUT TYPE	Single-ended or differential, AC coupled
IMPEDANCE	50 Ω (nom)	IMPEDANCE	50 Ω (nom)
AMPLITUDE	100 mVp-p to 1.2 Vp-p	AMPLITUDE	600mVpp,
AMPLITUDE RESOLUTION	±(3% of amplitude ±2 mV)		single-ended into 50 $\Omega$
VOLTAGE WINDOW	±2V	AMPLITUDE RESOLUTION	1mV
OFFSET RESOLUTION	1mV	AMPLITUDE	±(3% of amplitude ±2 mV)
DC OFFSET ACCURACY	±(2.0% of offset±10 mV)	ACCURACY BANDWIDTH	
SKEW BETWEEN NORMAL AND COMPLEMENT OUTPUTS	0 ps	-3dB analog BW 2ND Nyquist zone BW	100 kHz to 4.5 GHz (typ) Up to 9GHz
RISE/FALL TIME (20% TO 80%)	<150 ps (typ)	CONNECTOR TYPE	SMA
JITTER (PEAK-PEAK)	<15 ps (typ)	REFERENCE CLOCK OUTPUT	
OVERSHOOT	<5% (typ)	FREQUENCY	10MHz / 100MHz selectable
CONNECTOR TYPE	SMA	CONNECTOR	SMP
MARKER OUTPUTS	P9082/4/6D	P2582/4/8/12D	P1282/4/8/12D

MARKER OUTPUTS	P9082/4/6D	P2582/4/8/12D	P1282/4/8/12D	
NUMBER OF MARKERS	8/16/24	4/4/8/12		
OUTPUT TYPE	Single Ended			
OUTPUT IMPEDANCE	50 Ω (nom)			
LEVEL	100 mVp-p to 1.2 Vp-p with 40mV resolution			
RISE/FALL TIME (20% TO 80%)	<400ps			
MARKER TO DIRECT/DC OUT	<1SCLK			
WIDTH	User defined, in points			
DELAY CONTROL	Position control in points			
RANGE	0 - waveform length			
RESOLUTION	8 points 2 points			
CONNECTOR TYPE	SMP			

SYNC CLOCK OUTPUT		TRIGGER/GATE AND EVENT INPUT		
FREQUENCY	1/64 of the sample clock frequency	INPUT RANGE	±5 V -5 V to +5 V	
CONNECTOR	SMP	THRESHOLD RANGE RESOLUTION		
SAMPLE CLOCK OUTPUT	SAMPLE CLOCK OUTPUT		100 mV 200 mV	
SOURCE	Selectable, internal synthesizer	JITTER @ MAX CLOCK	3.2ns (200ps optional)	
	or sample clock input	POLARITY	Pos or Neg	
FREQUENCY RANGE	SCLK Range	SCLK Range DRIVE		
OUTPUT AMPLITUDE	400 mVpp (nom), fix		Selectable channel 1, channel 2 or both	
INPUT IMPEDANCE	50 $\Omega$ (nom), AC coupled	INPUT	1 k or 50 Ω (nom),	
AMPLITUDE ACCURACY	±(3% of amplitude ±2 mV)	IMPEDANCE	DC coupled	
TRANSITION TIME		MAX TOGGLE FREQUENCY	TBD	
(20% TO 80%)	20 ps (typ)	MINIMUM PULSE WIDTH	TBD	
CONNECTOR	SMA	CONNECTOR TYPE	SMP	





nfinite possibilities			ORDERI	NG INFORMATION
FAST SEGMENT DYNAMIC	CONTROL	NPUT (OPTIONAL)	MODEL	DESCRIPTION
			P1282D	1.25GS/s, 16Bit, AWG, 1GS/s Memory, 2CH, 4 Markers
NUMBER OF ADDRESSABLI SEGMENTS OR SEQUENCE		256	P1284D	1.25GS/s, 16Bit, AWG, 1GS/s Memory, 4CH, 4 Markers
DATA RATE	-	TBD	P1288D	1.25GS/s, 16Bit, 2GS Memory, 8CH 8 Markers
SET-UP TIME			P12812	D 1.25GS/s, 16Bit, 2GS Memory, 12CH 12 Markers
		TBD	P2582D	2.5GS/s, 16Bit, 2GS Memory 2CH, 8 Markers
HOLD TIME		TBD	P2584D	2.5GS/s, 16Bit, 2GS Memory, 4CH, 8 Markers
		0 V to +0.7 V	P2588D	2.5GS/s, 16Bit, 2GS Memory, 8CH 16 Markers
LOW LEVEL HIGH LEVEL		+1.6 V to +3.6 V	P25812	D 2.5GS/s, 16Bit, 2GS Memory, 12CH, 24 Markers
IMPEDANCE		TBD	P9082D	9GS/s 16Bit, 4GS Memory 2CH, 8 Markers
CONNECTOR		TBD	P9084D	9GS/s 16Bit, 4GS Memory 4CH, 16 Markers
CONNECTOR		001	P9086D	9GS/s 16Bit, 4GS Memory 6CH, 24 Markers
REFERENCE CLOCK INPUT			OPTION	
INPUT FREQUENCIES		10MHz / 100MHz	4M1	4GS Memory Opt. for models P1282D,& P2582D
		selectable	4M2	4GS Memory Opt. for models P1284D & P2584x
LOCK RANGE		± 1MHz	4M3	4GS Memory Opt. for models P1288D, P2588D & P9084D
INPUT LEVEL		0.2 Vp-p to 3.0 Vp-p	4M4	4GS Memory Opt. for models P12812D, P25812D & P9086D
IMPEDANCE		50 $\Omega$ , AC coupled (nom)	8M1	8GS Memory Opt. for models P1282D & P2582D
CONNECTOR TYPE		SMP	8M2	8GS Memory Opt. for models P1284D, P2584D & P9082D
SAMPLE CLOCK INPUT			8M3	8GS Memory Opt. for models P1288D, P2588D & P9084D
			8M4	8GS Memory Opt. for models P12812D, P25812D & P9086D
FREQUENCY RANGE		SCLK Range	16M1	16GS Memory option for models P9082D
INPUT POWER RANGE		+0 dBm to +7 dBm	16M2	16GS Memory option for models P9084D
DAMAGE LEVEL		+8 dBm	16M3	16GS Memory option for models P9086D
INPUT IMPEDANCE		50 $\Omega$ nom, AC coupled	DO1	9GHz BW Direct Output Opt. for models P1282D & P2582D
CONNECTOR TYPE		SMA	DO2	9GHz BW Direct Output Opt. for models P1284D, P2584D & P9082D
GENERAL			DO3	9GHz BW Direct Output Opt. for models P1288D, P2588D & P9084D
Voltage Range:		90VAC to 264VAC	DO4	9GHz BW Direct Output Opt. for models P12812D, P25812D & P9086D
Frequency Range:		47Hz to 63Hz	FS1	Fast Segment Control Opt.for models P1282D & P2582D
Power Consumption:		550W max.	FS2	Fast Segment Control Opt.for models P1284D, P2584D & P9082D
Interface:			FS3	Fast Segment Control Opt. for models P1288D, P2588D & P9084D
USB		ront, USB host (type A)	MRK1	x8 Extra Markers Opt. for models P1282D& P2582D
		rear USB host, (type A) Thunderbolt 3 USB (type C)	MRK2	x8 Extra Markers Opt. for models P1284D,P2584D & P9082D
		<ul> <li>Option TBolt</li> </ul>	MRK3	x16 Extra Markers Opt. for models P1288D, P2588D & P9084D
LAN	•	2 optional) x RG45 x rear		
		000/100/10 BASE-T optional SFP+ 10G Optical	LTJ1	Ultra Low Trigger Jitter (200ps typ.) Opt.for models P1282D & P2
GPIB		E 488.2 – Option GPIB	LTJ2	Ultra Low Trigger Jitter (200ps typ.) Opt.for models P1284D, P2584D & P9082
Storage		128GB removable	LTJ3	Ultra Low Trigger Jitter (200ps typ.) Opt. for models P1288D, P2588D & P9084
Dimensions:		7.5 Kg	LTJ4	Ultra Low Trigger Jitter (200ps typ.) Opt. for models P12812D, P25812D & P90
Without Package Shipping Weight		9 Kg	G1	Low Waveform Granularity Opt. for models P1282D & P2582D
Weight:			G2	Low Waveform Granularity Opt. for models P1284D, P2584D & P
Operating		0°C to +40°C	G3	Low Waveform Granularity Opt. for models P1288D, P2588D & P9
Storage Temperature:		-40°C to +70°C	G4	Low Waveform Granularity Opt. for models P12812D, P25812D & P908
Operating		0°C to +40°C	AWT1	
Storage Warm up time		-40°C to +70°C 15 minutes		9GHz BW, 2.7GS/s 12 Bit 1CH Digitizer Opt. for models P1282D & P258
Humidity:	85%	6 RH, non-condensing	AWT2	9GHz BW, 2.7GS/s 12 Bit 1CH Digitizer Opt. for models P1284D, P2584D & P
Safety:	CE M	arked, EC61010-1:2010	AWT3	9GHz BW, 2.7GS/s 12 Bit 2CH Digitizer Opt. for models P1288D,P2588D & P90
EMC:		IEC 61326-1:2013	FPGA PROG	FPGA Programming Capability with built-in Demodulation & digital F
Calibration:		2 years	ARTIQ	ADTIO Caro integration to allow circuly EDCA control 9 per succession
Warranty:	1/	3year warranty plan	Core	ARTIQ Core integration to allow simply FPGA control & programming

